

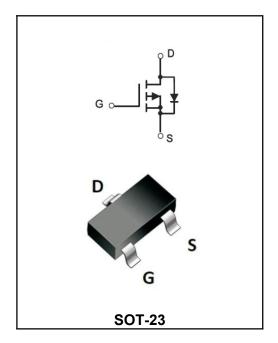
## -20V P-CHANNEL ENHANCEMENT MODE MOSFET

#### **MAIN CHARACTERISTICS**

<b>I</b> <sub>D</sub>	-4.9A		
V <sub>DSS</sub>	-20V		
R <sub>DSON</sub> -typ(@V <sub>GS</sub> =-4.5V)	< 38mΩ <b>(Type:32 mΩ)</b>		

#### **Application**

- **♦**Battery protection
- **♦**Load switch
- ♦Uninterruptible power supply



Marking Code				
YFW2305A	A5SHB			

## Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Symbols	Value	Units
Drain-Source Voltage	V <sub>DS</sub>	-20	V
Gate - Source Voltage	V <sub>GS</sub>	±12	V
Continuous Drain Current, V <sub>GS</sub> @ -4.5V <sup>1</sup> @T <sub>A</sub> =25°C	I <sub>D</sub>	-4.9	Α
Continuous Drain Current, V <sub>GS</sub> @ -4.5V¹ @T <sub>A</sub> =70℃	I <sub>D</sub>	-3.9	Α
Pulsed Drain Current <sup>2</sup>	Ідм	-14	Α
Total Power Dissipation³ @T <sub>A</sub> =25℃	P <sub>D</sub>	1.31	w
Total Power Dissipation³ @T <sub>A</sub> =70℃	P <sub>D</sub>	0.84	w
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C
Operating Junction Temperature Range	TJ	-55 to +150	°C
Thermal Resistance Junction-Ambient <sup>1</sup>	R <sub>0JA</sub>	120	°C/W
Thermal Resistance Junction-Ambient <sup>1</sup> (t ≤10s)	R <sub>0JA</sub>	95	°C/W



# Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Test Condition	Symbols	Min	Тур	Max -	Units V	
Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	BV <sub>DSS</sub>	-20				
BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA	∆BV <sub>DSS/∆TJ</sub>	-	-0.014	-	V/°C	
	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4.9A		-	32	38	mΩ	
Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3.4A	R <sub>DS(ON)</sub>	-	45	55		
	V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-2A			65	85	1	
Gate -Threshold Voltage	)/ =\/ L = 250A	V <sub>GS(th)</sub>	-0.4	-	-1.0	V	
V <sub>GS</sub> (th) Temperature Coefficient	$V_{DS}=V_{GS}$ , $I_D=-250uA$	△V <sub>GS(th)</sub>	-	3.95	-	mV/°C	
Darin Carras Laskana Comunt	V <sub>DS</sub> =-16V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃		-	-	-1	- μΑ	
Drain-Source Leakage Current	V <sub>DS</sub> =-16V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃	loss -	-	-	-5		
Gate –Source Leakage Current	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	I <sub>GSS</sub>	-	-	±100	nA	
Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-3A	g <sub>fs</sub>	-	12.8	-	S	
Total Gate Charge(-4.5V)	\/ - 15\/	Qg	-	10.2	14.3		
Gate-Source Charge	V <sub>DS</sub> =-15V V <sub>GS</sub> =-4.5V	Q <sub>gs</sub>	-	1.89	2.6	nC	
Gate-Drain Charge	I <sub>D</sub> =-3A	Q <sub>gd</sub>	-	3.1	4.3		
Turn-on delay time		t <sub>d(on)</sub>	-	5.6	11.2		
Rise Time	$V_{DD} = -10V$ $V_{GS} = -4.5V$	T <sub>r</sub>	-	40.8	73		
Turn-Off Delay Time	$I_D$ = -3A $R_G$ =3.3	t <sub>d(OFF)</sub>	-	33.6	67	- ns	
Fall Time	- N <sub>G</sub> -3.3	t <sub>f</sub>	-	18	36		
Input Capacitance	V <sub>DS</sub> =-15V	C <sub>iss</sub>	-	857	1200		
Output Capacitance	V <sub>GS</sub> =0V	Coss	-	114	160	PF	
Reverse Transfer Capacitance	f=1MHz	C <sub>rss</sub>	-	108	151	1	
Continuous Source Current <sup>1,4</sup>		Is	-	-	-4.9	Α	
Pulsed Source Current <sup>2,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	I <sub>SM</sub>	-	-	-14	Α	
Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25℃	V <sub>SD</sub>	-	-	-1	v	
Reverse Recovery Time		t <sub>rr</sub>	-	21.8	-	ns	
Reverse Recovery Charge	I <sub>F</sub> =-3A, dI/dt=100A/μs, T <sub>J</sub> =25℃	Q <sub>rr</sub>	-	6.9	-	nC	

Note:

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<sup>1 .</sup>The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

<sup>2.</sup>The data tested by pulsed , pulse width  $\triangle$  300us , duty cycle  $\triangle$  2%

<sup>3.</sup>The power dissipation is limited by 150  $^{\circ}\mathrm{C}$  junction temperature

<sup>4.</sup>The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation.



#### **Ratings and Characteristic Curves**

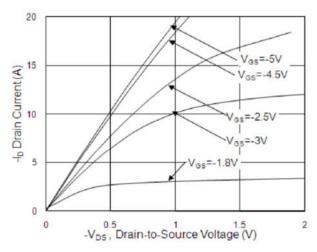


Fig.1 Typical Output Characteristics

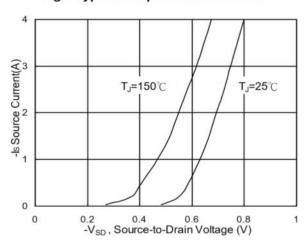


Fig.3 Forward Characteristics of Reverse

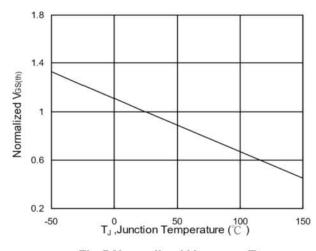


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

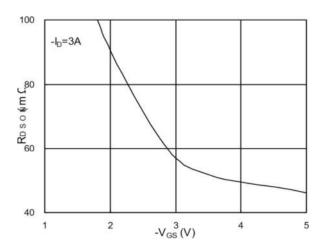


Fig.2 On-Resistance vs. G-S Voltage

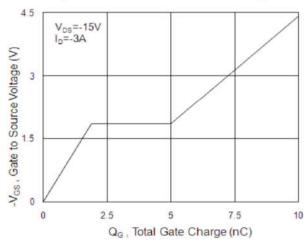


Fig.4 Gate-charge Characteristics

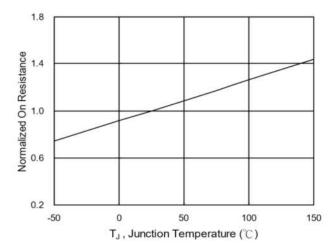
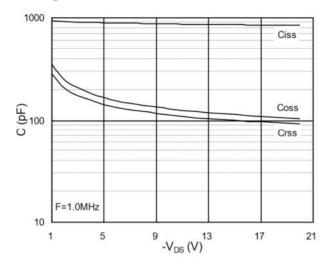


Fig.6 Normalized  $R_{DSON}$  vs.  $T_J$ 



#### **Ratings and Characteristic Curves**



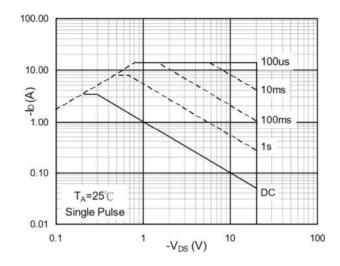


Fig.7 Capacitance

Fig.8 Safe Operating Area

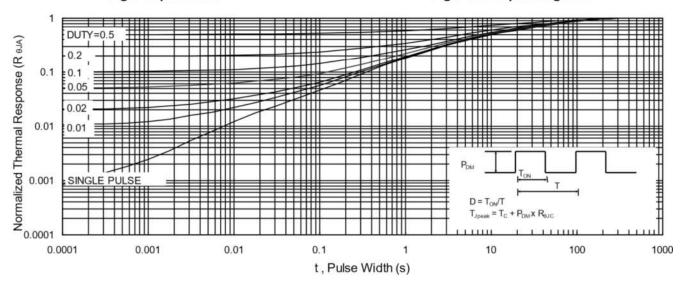
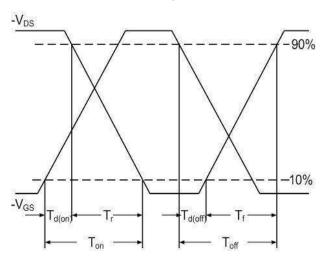
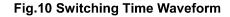


Fig.9 Normalized Maximum Transient Thermal Impedance





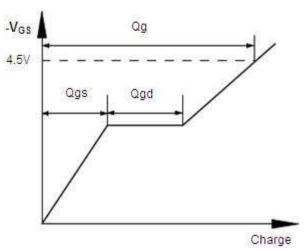


Fig.11 Gate Charge Waveform

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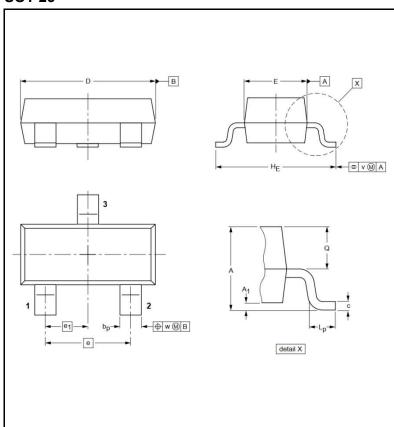


## **Ordering information**

Package Packing Description		Base Quantity	Packing Quantity	
SOT-23	Tape/Reel,7"reel	3000pcs/Reel	24000PCS/Box 120000PCS/Carton	

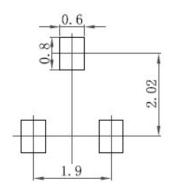
## **Package Dimensions**

## SOT-23



Dim.	Millimeter (mm)		mil	
	Min.	Max.	Min.	Max.
Α	0.9	1.15	35	45
A1	0.1		3.9	)
bp	0.38	0.48	15	19
С	0.09	0.15	3.54	5.9
D	2.8	3.0	110	118
Е	1.2	1.4	47	55
Е	1.9		75	
E1	0.95		37	,
HE	2.1	2.55	83	100
Lp	0.15	0.45	5.9	18
Q	0.45	0.55	18	22
V	0.2		7.9	
W	0.1		4	

# The recommended mounting pad size





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