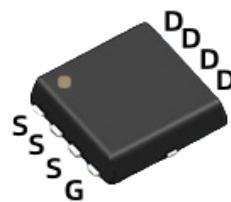
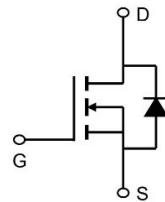


40V N-CHANNEL ENHANCEMENT MODE MOSFET
MAIN CHARACTERISTICS

I_D	60A
V_{DSS}	40V
$R_{DS(ON)-typ}(@V_{GS}=10V)$	<7.5mΩ(Typ:5.5 mΩ)
$R_{DS(ON)-typ}(@V_{GS}=4.5V)$	<12mΩ(Typ:9.0 mΩ)


PDFN3*3-8L
Description

The YFW60N04DF uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a battery protection or in other Switching application.

Application

- ◆ Battery protection
- ◆ Load switch
- ◆ Uninterruptible power supply

Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Characteristics		Symbols	Value	Units
Drain-Source Voltage		V_{DS}	40	V
Gate - Source Voltage		V_{GS}	±20	V
Continuous Drain Current ¹	@T _c =25°C	I_D	60	A
Continuous Drain Current ¹	@T _c =100°C	I_D	35	A
Pulsed Drain Current ²		I_{DM}	90	A
Single Pulse Avalanche Energy ³		E_{AS}	76.1	mJ
Avalanche Current		I_{AS}	39	A
Power dissipation ⁴	T _c =25 °C	P_D	28	W
Thermal Resistance Junction-Ambient (Steady State) ¹		$R_{θJA}$	62	°C/W
Thermal Resistance Junction-Case ¹		$R_{θJC}$	4.5	°C/W
Storage Temperature Range		T_{STG}	-55 to +150	°C
Operating Junction Temperature Range		T_J	-55 to +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu\text{A}$	BV_{DSS}	40	45	-	V
BVDSS Temperature Coefficient ²	Reference to $25^\circ\text{C}, I_D=1\text{mA}$	$\Delta BV_{DSS}/\Delta T_J$	-	0.034	-	V/°C
Drain-Source Leakage Current	$V_{DS}=32\text{V}, V_{GS}=0\text{V}, T_J=25^\circ\text{C}$	I_{DSS}	-	-	1.0	μA
	$V_{DS}=32\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$		-	-	5.0	μA
Gate-Source Leakage Current	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$	I_{GSS}	-	-	± 100	nA
Gate -Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	$V_{GS(\text{th})}$	1.0	1.7	2.5	V
$V_{GS(\text{th})}$ Temperature Coefficient		$\Delta V_{GS(\text{th})}$	-	4.96	-	mV/°C
Drain-source on-state resistance ²	$V_{GS}=10\text{V}, I_D=30\text{A}$	$R_{DS(\text{ON})}$	-	5.5	7.5	mΩ
	$V_{GS}=4.5\text{V}, I_D=20\text{A}$		-	9.0	12	
Forward Transconductance	$V_{DS}=5\text{V}, I_D=12\text{A}$	g_{fs}	-	39	-	S
Gate Resistance	$V_{DS}=0\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	R_g		1.6	-	Ω
Input Capacitance	$V_{DS}=15\text{V}$ $V_{GS}=0\text{V}$ $f=1\text{MHz}$	C_{iss}	-	2332	-	pF
Output Capacitance		C_{oss}	-	193	-	
Reverse Transfer Capacitance		C_{rss}	-	138	-	
Total Gate Charge	$V_{DS}=20\text{V}$ $I_D=12\text{A}$ $V_{GS}=4.5\text{V}$	Q_g	-	18.8	-	nC
Gate-Source Charge		Q_{gs}	-	4.7	-	
Gate Drain Charge		Q_{gd}	-	8.2	-	
Turn-On Delay Time	$V_{DD}=15\text{V}$ $V_{GS}=10\text{V}$ $R_G=3.3\Omega$ $I_D=1\text{A}$	$t_{d(on)}$	-	14.3	-	ns
Rise Time		T_r	-	2.6	-	
Turn-Off Delay Time		$t_{d(OFF)}$	-	77	-	
Fall Time		t_f	-	4.8	-	
Continuous Source Current ^{1.5}	$V_G=V_D=0\text{V}$, Force Current	I_s	-	-	60	A
Pulsed Source Current ^{2.5}		I_{SM}	-	-	90	A
Diode Forward Voltage ²	$V_{GS}=0\text{V}, I_S=1\text{A}, T_J=25^\circ\text{C}$	V_{SD}	-	-	1	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25\text{V}, V_{GS}=10\text{V}, L=0.1\text{mH}, IAS=39\text{A}$
- 4 .The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics

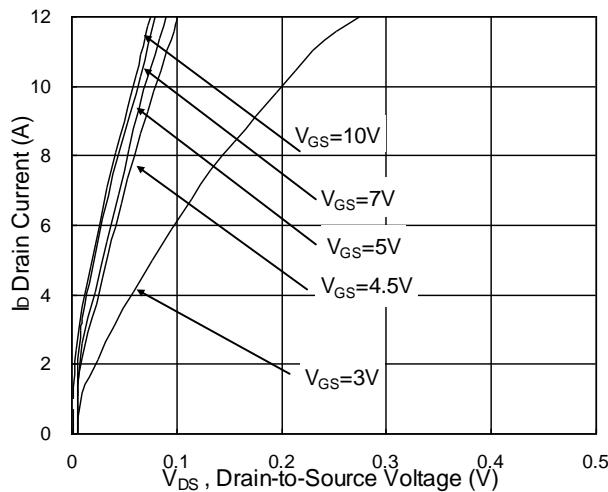


Fig.1 Typical Output Characteristics

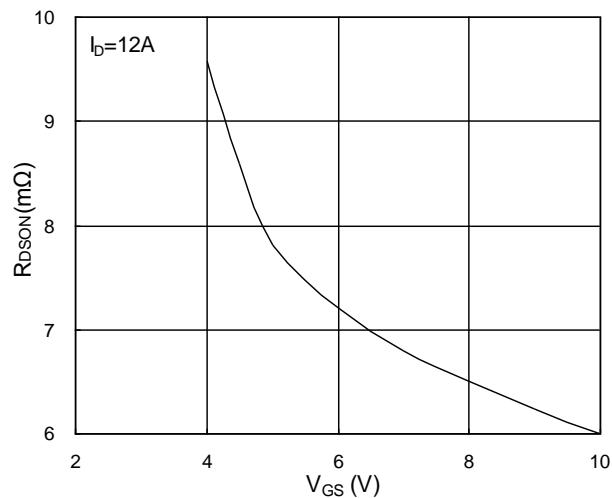


Fig.2 On-Resistance vs. G-S Voltage

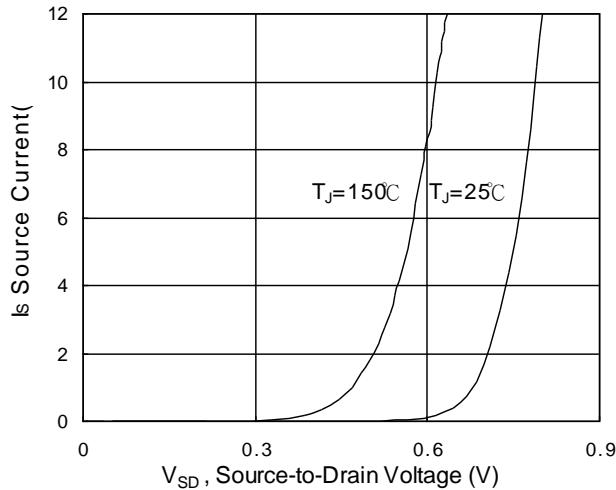


Fig.3 Forward Characteristics of Reverse

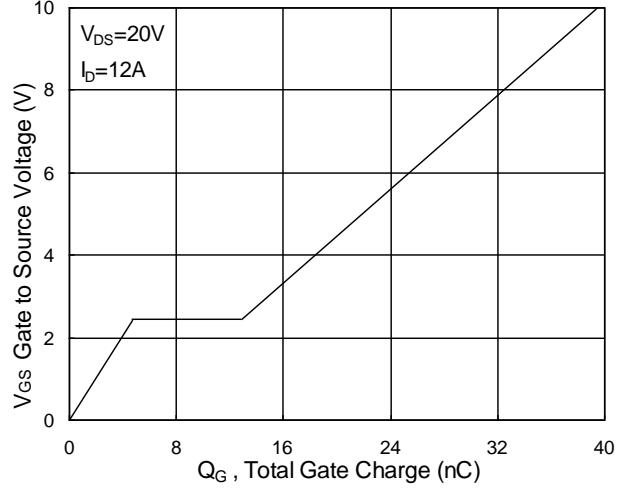


Fig.4 Gate-Charge Characteristics

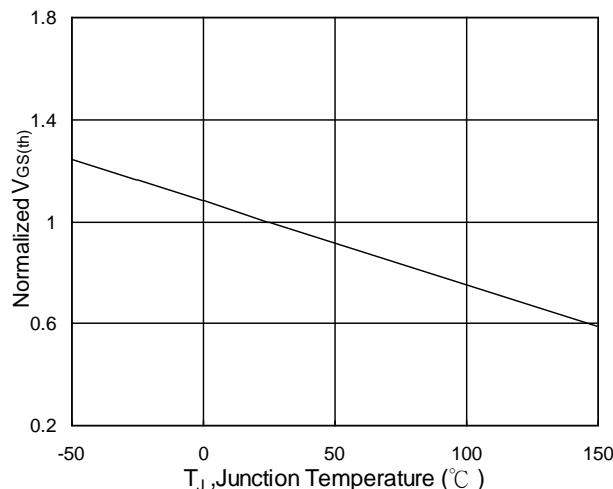


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

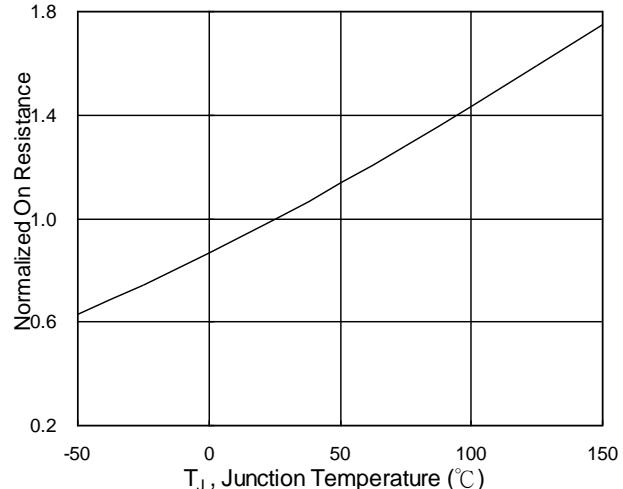


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

Typical Characteristics

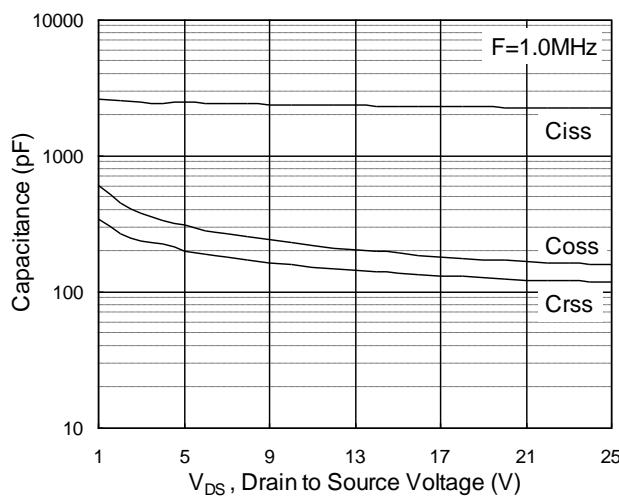


Fig.7 Capacitance

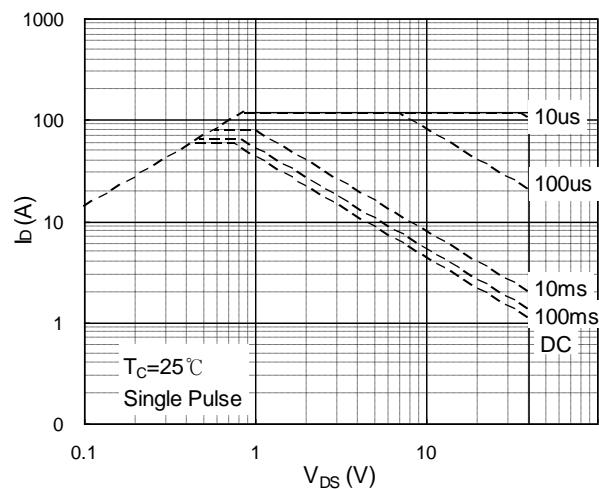


Fig.8 Safe Operating Area

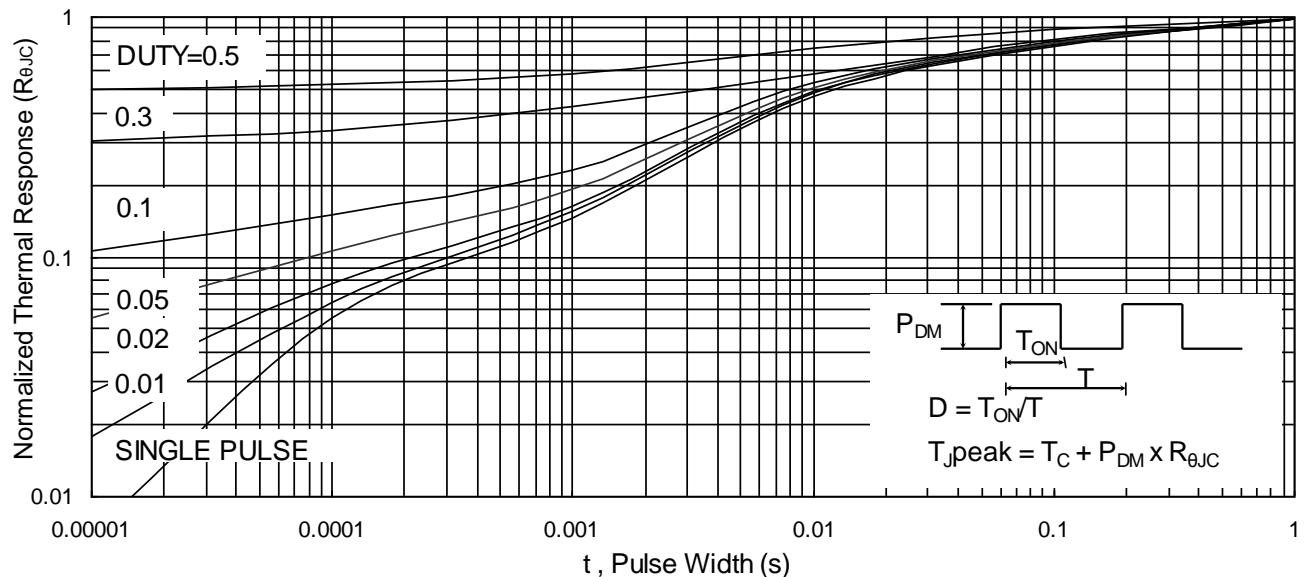


Fig.9 Normalized Maximum Transient Thermal Impedance

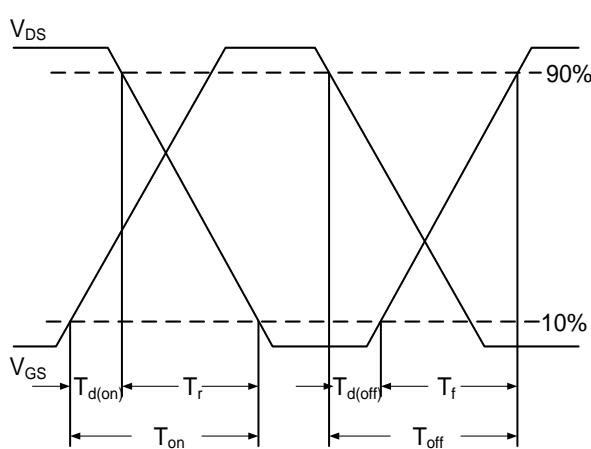


Fig.10 Switching Time Waveform

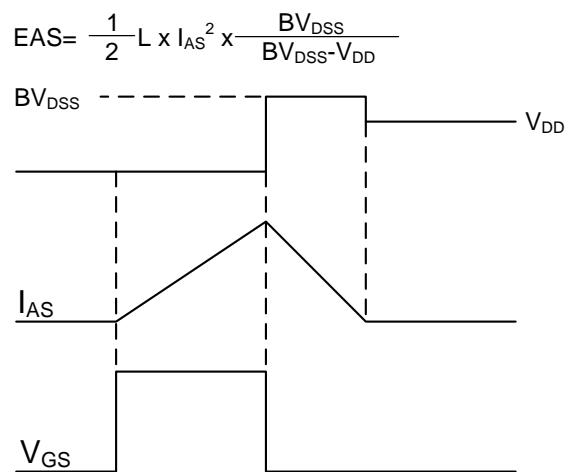
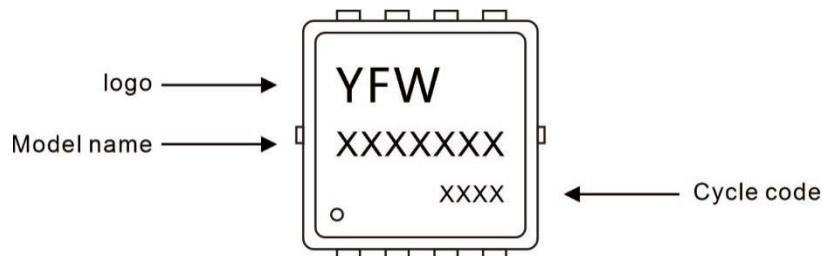


Fig.11 Unclamped Inductive Waveform

Marking Diagram



Ordering information

Model name	Package	Unit Weight	Base Quantity	Packing Quantity
YFW60N04DF	PDFN3*3-8L	0.0023oz(0.065g)	5000pcs/reel	10000pcs/box 50000pcs/Carton

Package Dimensions

PDFN3*3-8L

Dim	Millimeter		mil	
	Min.	Max.	Min.	Max.
A	0.70	0.85	0.0276	0.0335
A1	-	0.05	-	0.002
b	0.20	0.40	0.008	0.016
c	0.10	0.25	0.004	0.010
D	3.15	3.45	0.124	0.136
D1	3.00	3.25	0.118	0.128
D2	2.29	2.65	0.09	0.104
E	3.15	3.45	0.124	0.136
E1	2.90	3.20	0.114	0.126
E2	1.54	1.94	0.061	0.076
E3	0.28	0.65	0.011	0.026
E4	0.37	0.77	0.015	0.030
E5	0.10	0.30	0.004	0.012
e	0.60	0.70	0.024	0.028
K	0.59	0.89	0.023	0.035
L	0.30	0.50	0.012	0.020
L1	0.06	0.20	0.002	0.008
t	-	0.13	-	0.005
Φ	10°C	14°C	10°C	14°C

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