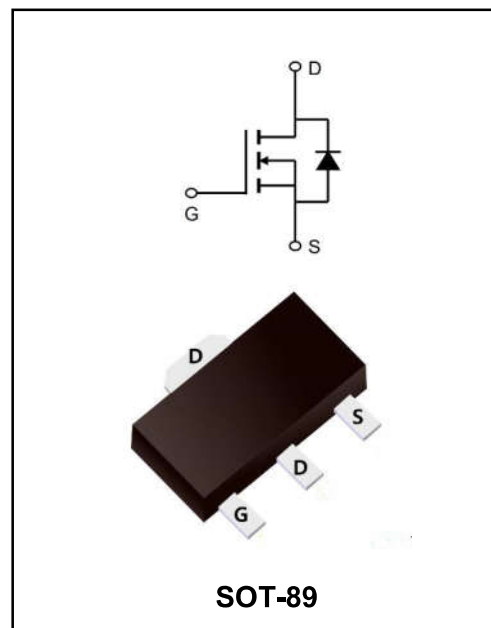


60V N-CHANNEL ENHANCEMENT MODE MOSFET

MAIN CHARACTERISTICS

I_D	4.8A
V_{DS}	60V
$R_{DS(on)-typ}(@V_{GS}=10V)$	< 95mΩ (Typ: 72 mΩ)



Application

- ◆ Battery protection
- ◆ Load switch
- ◆ Uninterruptible power supply

Marking Code

YFW4N06SI

YFW 4N06

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Symbols	Value	Units
Drain-Source Voltage	V_{DS}	60	V
Gate - Source Voltage	V_{GS}	±20	V
Continuous Drain Current, V_{GS} @ 10V ¹ @T _C =25°C	I_D	4.8	A
Continuous Drain Current, V_{GS} @ 10V ¹ @T _C =100°C	I_D	2.0	A
Pulsed Drain Current ²	I_{DM}	15	A
Single Pulse Avalanche Energy ³	E_{AS}	6.2	mJ
Total Power Dissipation ⁴ @T _C =25°C	P_D	1.5	W
Storage Temperature Range	T_{STG}	-55 to +150	°C
Operating Junction Temperature Range	T_J	-55 to +150	°C
Thermal Resistance, Junction-to-Ambient ¹	$R_{\theta JA}$	85	°C/W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	48	°C/W

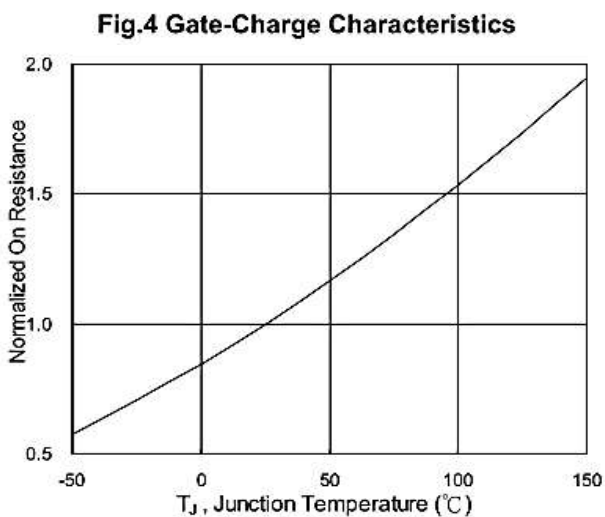
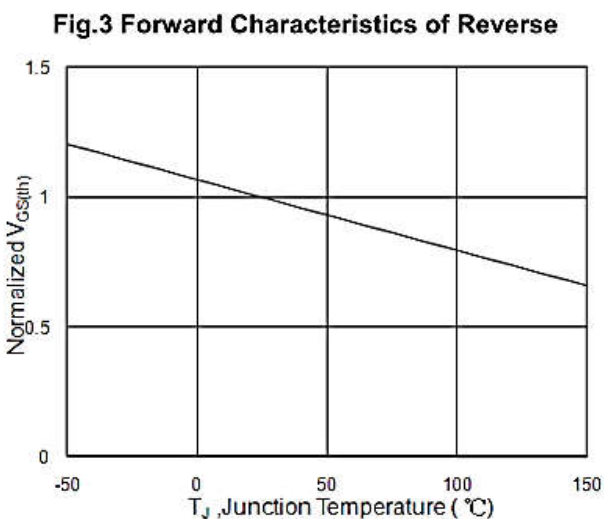
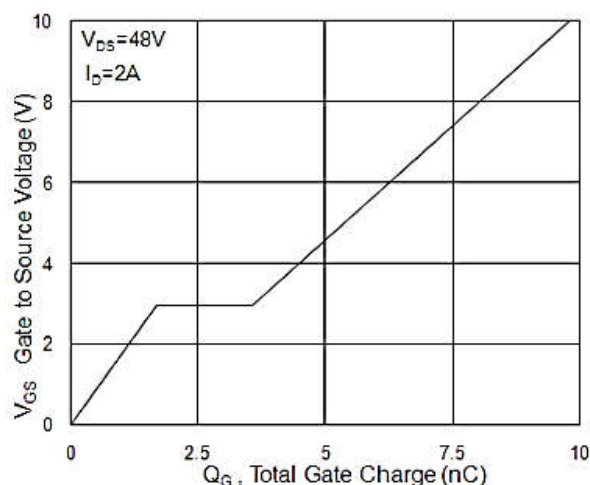
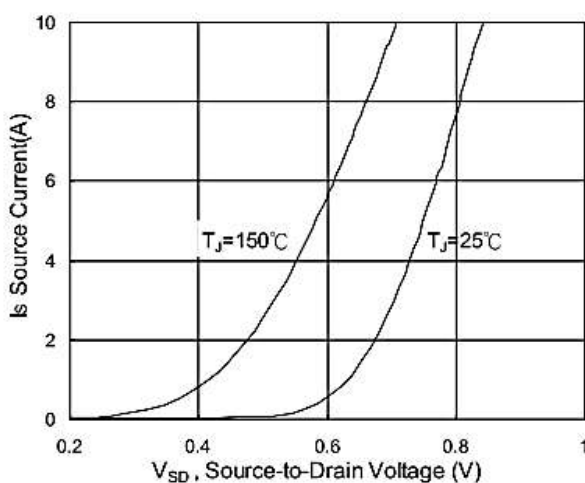
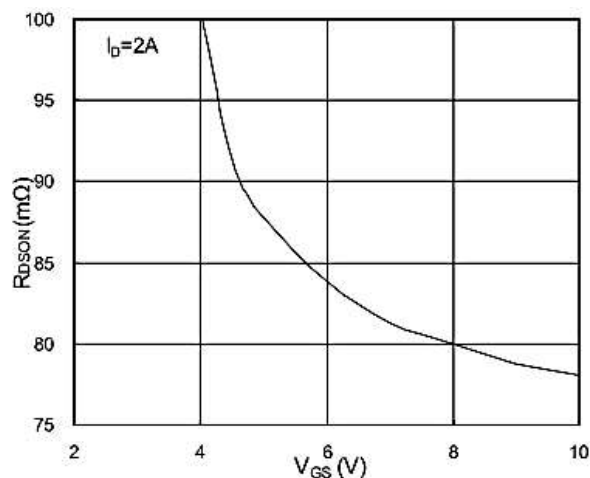
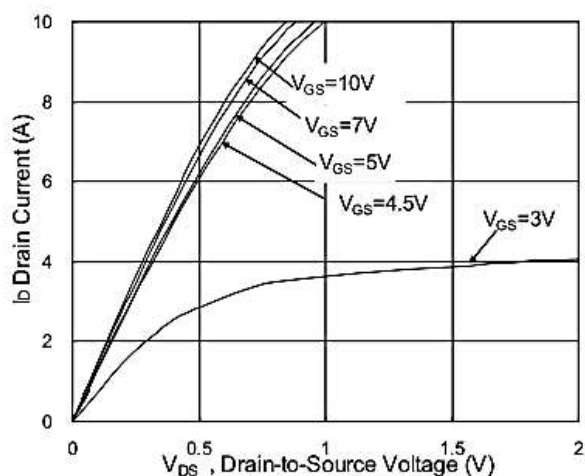
Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	BV_{DSS}	60	65	-	V
BVDSS Temperature Coefficient	Reference to 25°C, $I_D=1mA$	$\Delta BV_{DSS}/\Delta T_J$	-	0.054	-	V/°C
Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=2A$	$R_{DS(ON)}$	-	72	95	mΩ
	$V_{GS}=4.5V, I_D=1A$		-	85	100	
Gate -Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	$V_{GS(th)}$	1.2	1.5	2.5	V
VGS(th) Temperature Coefficient		$\Delta V_{GS(th)}$	-	-4.96	-	mV/°C
Drain-Source Leakage Current	$V_{DS}=48V, V_{GS}=0V, T_J=25^\circ C$	I_{DSS}	-	-	1	μA
	$V_{DS}=48V, V_{GS}=0V, T_J=55^\circ C$		-	-	5	
Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	I_{GSS}	-	-	±100	nA
Forward Transconductance	$V_{DS}=5V, I_D=2A$	g_{FS}	-	13	-	S
Total Gate Charge(4.5V)	$V_{DS}=48V$ $V_{GS}=4.5V$ $I_D=2A$	Q_g	-	5	7.0	nC
Gate-Source Charge		Q_{GS}	-	1.68	2.4	
Gate-Drain Charge		Q_{GD}	-	1.9	2.7	
Turn-on delay time	$V_{DD}=30V$ $V_{GS}=10V$ $R_G=3.3\Omega$ $I_D=2A$	$t_{d(on)}$	-	1.6	3.2	ns
Rise Time		T_r	-	7.2	13	
Turn-Off Delay Time		$t_{d(OFF)}$	-	25	50	
Fall Time		t_f	-	14.4	28.8	
Input Capacitance	$V_{DS}=15V$ $V_{GS}=0V$ $f=1.0MHz$	C_{iss}	-	511	715	pF
Output Capacitance		C_{oss}	-	38	53	
Reverse Transfer Capacitance		C_{rss}	-	25	35	
Continuous Source Current ^{1,4}	$V_G=V_D=0V$, Force Current	I_S	-	-	2.3	A
Pulsed Source Current ^{2,4}		I_{SM}	-	-	9.2	A
Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^\circ C$	V_{SD}	-	-	1.2	V
Reverse Recovery Time	$I_F=2A, dI/dt=100A/\mu s$, $T_J=25^\circ C$	t_{rr}	-	9.7	-	ns
Reverse Recovery Charge		Q_{rr}	-	5.8	-	nC

Note :

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3、The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=2A$
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Ratings and Characteristic Curves



Ratings and Characteristic Curves

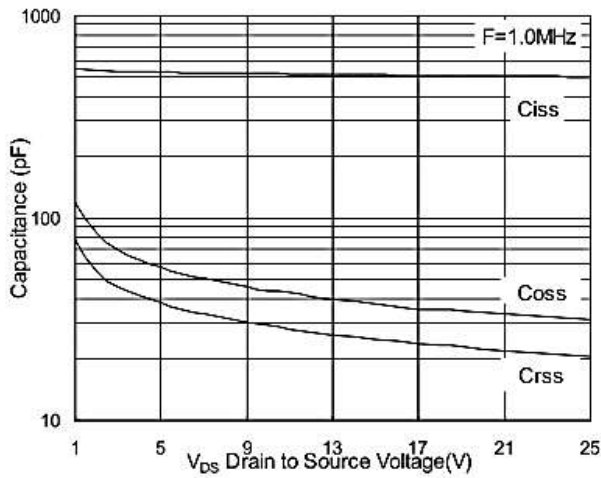


Fig.7 Capacitance

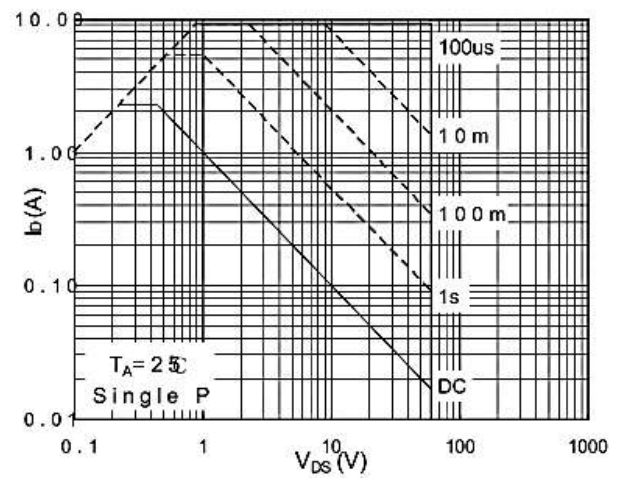


Fig.8 Safe Operating Area

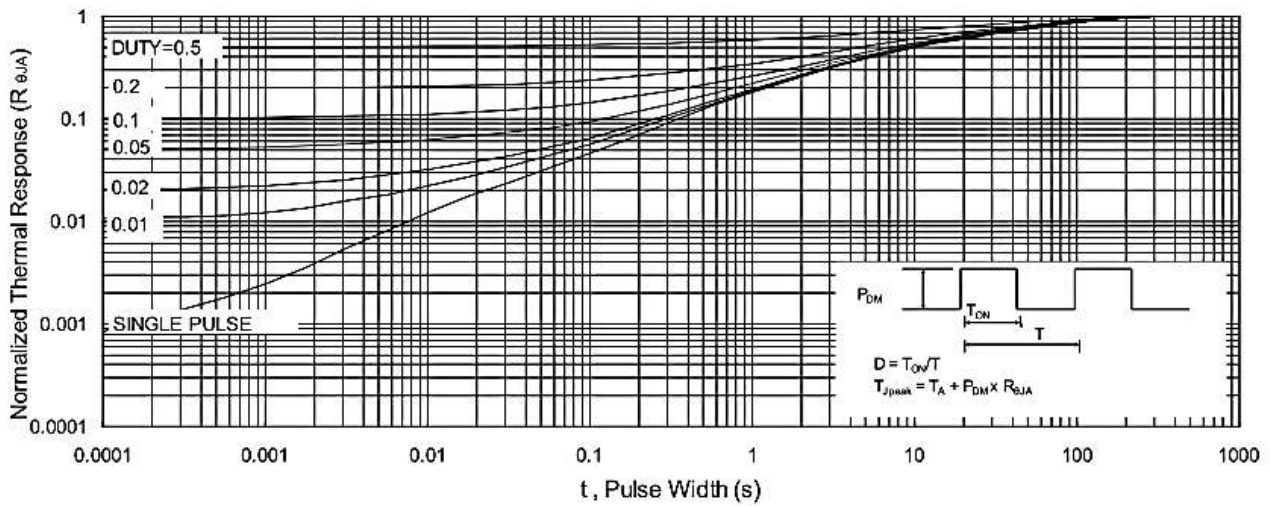


Fig.9 Normalized Maximum Transient Thermal Impedance

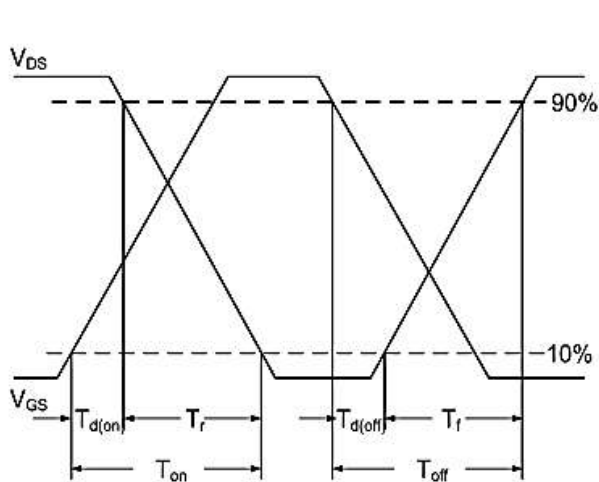


Fig.10 Switching Time Waveform

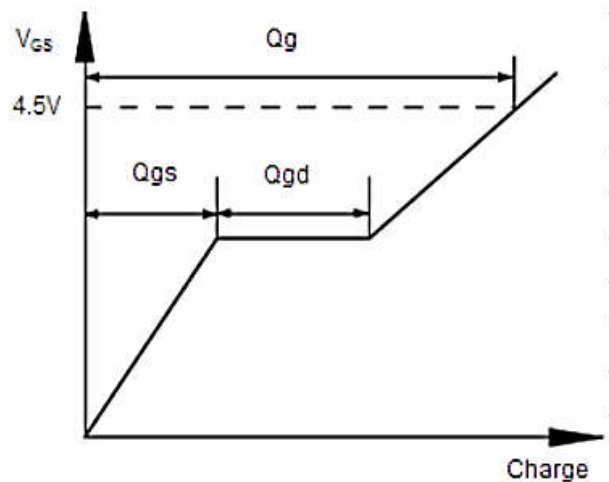


Fig.11 Gate Charge Waveform

Ordering information

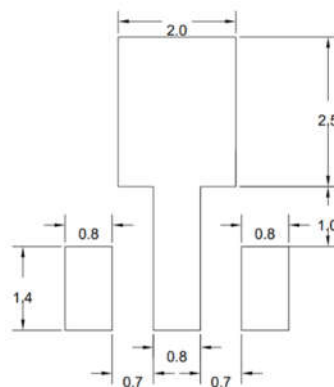
Package	Packing Description	Base Quantity	Packing Quantity
SOT-89	Tape/Reel, 7" reel	1000pcs/Reel	6000PCS/Box 30000PCS/Carton

Package Dimensions

SOT-89

Dim	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	1.40	1.60	0.055	0.063
b	0.32	0.52	0.013	0.020
b1	0.38	0.58	0.015	0.023
c	0.35	0.45	0.014	0.018
D	4.40	4.60	0.173	0.181
D1	1.45	1.65	0.057	0.065
D2	1.70	1.80	0.067	0.071
E	2.30	2.60	0.091	0.102
E1	3.95	4.25	0.156	0.167
E2	1.80	2.00	0.071	0.079
e	1.40	1.60	0.055	0.063
e1	2.80	3.20	0.110	0.126
L	0.90	1.20	0.035	0.047

The recommended mounting pad size



UNIT:MM

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